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Transmitted herewith for filing is the patent application of:  
Inventor: Anderson

For: DYNAMIC BASS EQUALIZATION WITH MODIFIED SALLEN-KEY HIGH PASS FILTER

Enclosed are:


- ☒ Patent application with 13 pages of specification, 4 pages of claims, an abstract, and 5 sheets of drawings.
- ☒ Combined Declaration and Power of Attorney
- ☒ Information Disclosure Statement including Form PTO-1449 and two references
- ☒ Assignment and Assignment cover sheet

CLAIMS AS FILED					
For	Number Filed		Number Extra	Rate	Basic Fee \$760.00
Total claims	30	-20	10	x \$18.00 =	\$180
Independent Claims	4	-3	1	x 78.00 =	\$78
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Dynamic Bass Equalization with  
Modified Sallen-Key High Pass Filter

5        Field of the Invention

The present invention relates to multimedia computer amplified speaker systems and more particularly to a system with a dynamic bass equalization circuit having amplitude dependent gain and a dynamically adjusted frequency response that varies with the amplitude dependent gain.

10       Background and Summary of the Invention

Speaker systems with integrated amplification electronics provide simple, compact audio transducers for multimedia personal computers. These speaker systems, referred to herein as multimedia computer amplified speaker systems, typically include pairs of wide-band speaker drivers mounted in separate housings with amplification electronics incorporated into one or both housings. Because compactness is desirable, small, wide-band speaker drivers (e.g., 3-inch diameter cone speaker drivers) are commonly used. Speaker systems of this type can offer acceptable stereo sound quality for some applications, but discerning listeners and demanding applications can require audio performance beyond the capabilities of such systems.

Bass sounds (i.e., sounds of frequencies less than about 150 Hz) are the most difficult for compact wide-band speaker systems to reproduce. The production of satisfactory bass levels is problematic for two reasons. Small speaker drivers have poor bass response because of their small physical size. In addition, the human ear is relatively less sensitive to bass frequencies than higher treble frequencies, particularly at low sound levels or volumes. The transduction of treble frequencies, which are designated herein as frequencies greater than about 150 Hz, does not suffer these difficulties.

In order to provide full, rich bass sound from a relatively small driver in a small enclosure, as in computer multi-media speaker applications, the electronics of the driving circuit are equalized to add a bass boost that compensates for limitations of driver size and enclosure volume. This bass  
5 boost can introduce artifacts into the audio signal that is delivered to the driver. For example, with most of the power provided by amplifier circuits typically being directed to bass frequencies, there is typically little capacity for increased volume levels. As a consequence, increasing the volume level will frequently result in amplifier clipping or sound distortion from driver over-  
10 excursion.

Past solutions to this problem have been to dynamically adjust the amount of bass boost relative to the desired loudness. As the volume is turned up, the bass boost, or relative bass level, is turned down. This allows the apparent loudness of the speaker system to increase without increasing  
15 undesired distortion levels. Typical circuit topologies to provide these dynamic adjustments generally fall into two classes: amplitude controlled variable resistance circuits and amplitude compressors/soft clippers. Both circuit types suffer from disadvantages.

Amplitude controlled variable resistance circuits suffer from associated  
20 attack and decay time delays during which bass boost adjustments are incomplete, resulting in discernible audio artifacts until the adjustments are completed. In addition, these types of circuits can introduce discernible distortion into the resulting audio signal. Amplitude compressors/soft clippers provide no control over frequency response. The general frequency response  
25 is largely unaffected by the compressing action. The fundamental frequencies are passed through to the amplifier, and the low frequencies can cause over-excursion in the driver.

Examples of prior dynamic equalization systems are illustrated by U.S. Patent No. 5,359,665 of Werrbach and U.S. Patent No. 5,361,381 of Short. Both of these systems employ control signals that are generated by an active circuit component to control signal compression or equalization. The  
5 generation of such control signals is delayed with respect to the signal being controlled, thereby introducing attack and decay time delays during signal compression or equalization. These attack and decay time delays result in discernible audio artifacts until the adjustments are completed.

Accordingly, a dynamic bass equalization circuit of the present  
10 invention has an amplitude dependent gain that is dependent upon the audio electrical signal amplitude and a dynamically adjusted frequency response that varies with the amplitude dependent gain. In one implementation, the dynamic bass equalization circuit includes a Sallen-Key high pass filter that includes an amplifier with a real-time negative feedback path. The  
15 dynamically adjusted frequency response is provided by a parallel pair of reversed diodes that are connected in the negative feedback path.

A dynamic bass equalization circuit according to the present invention operates without the attack or decay times that are characteristic of prior amplitude controlled variable resistance bass equalization circuits and  
20 equalization circuits employing active circuitry for generating control signals. In addition, the bass equalization circuit of this invention has amplitude dependent gain and frequency response modification that are not available in prior amplitude compressors/soft clipper bass equalization circuits. As a result, the present invention provides dynamic bass boost without the acoustic  
25 artifacts characteristic of attack or decay times or the over-excursion of speaker drivers that can occur with equalizers having fixed frequency characteristics.

Additional objects and advantages of the present invention will be apparent from the detailed description of the preferred embodiment thereof, which proceeds with reference to the accompanying drawings.

#### Brief Description of the Drawings

5        Fig. 1 is a block diagram of a multimedia computer stereophonic amplified speaker system with treble and bass speaker drivers.

      Fig. 2 is a circuit schematic diagram of a first embodiment of a dynamic bass equalization circuit with amplitude dependent gain and frequency response modification according to the present invention.

10       Fig. 3 is a circuit schematic diagram of a prior art amplitude controlled variable resistance bass equalization circuit.

      Fig. 4 is a circuit schematic diagram of a prior art amplitude compressors/soft clipper.

15       Fig. 5 is a circuit schematic diagram of a second embodiment of a dynamic bass equalization circuit with amplitude dependent gain and frequency response modification according to the present invention.

      Fig. 6 is a circuit schematic diagram of a third embodiment of a dynamic bass equalization circuit with amplitude dependent gain and frequency response modification according to the present invention.

20       Fig. 7 is a circuit schematic diagram of a dynamic bass equalization circuit with a fourth order Sallen-Key high pass filter and providing amplitude dependent gain and frequency response modification according to the present invention.

25       Fig. 8 is a circuit schematic diagram of a dynamic bass equalization circuit with a multiple feedback band-pass filter and providing amplitude dependent gain and frequency response modification according to the present invention.

Fig. 9 is a block diagram of a multimedia computer stereophonic amplified speaker system with full-range speaker drivers.

Detailed Description of Preferred Embodiments

Fig. 1 is a block diagram of a stereophonic amplified speaker system 10 that receives at respective inputs 14 and 16 audio electrical signals corresponding to left and right channels of a stereophonic audio electrical signal provided by a multimedia computer 12. The audio electrical signals received at inputs 14 and 16 are added together or mixed by an analog adder circuit 18.

A dynamic bass equalization circuit 20 with amplitude dependent gain and frequency response dynamically enhances or boosts the bass portion audio signal by an amount dependent on the magnitude of the audio electrical signal. The bass portion is boosted proportionally more for lower magnitude sound levels. In the present implementation, bass equalization circuit 20 operates as an active amplifier circuit that enhances the bass portion relative to the treble portion. A low-pass filter block 21 filters the audio signal from bass equalization circuit 20, blocking treble frequencies while permitting the boosted bass frequencies of the signal to pass to a bass amplifier 22.

The left and right stereophonic audio electrical signals continue on to respective high-pass filter blocks 24 and 26, which block bass frequencies while permitting treble frequencies of the left and right channel audio signals to pass to treble portion equalizers 27 and 29 and buffer amplifiers 28 and 30, respectively. Sub-woofer 32 and satellite treble-band speakers 34 and 36 receive corresponding audio signals from respective amplifiers 22, 28, and 30 to transduce the signals into sound. Speaker system 10 also includes at least one volume control (not shown) for controlling the volume of sound generated by system 10.

Fig. 2 is a circuit schematic diagram of a dynamic bass equalization circuit 20 with amplitude dependent gain and frequency response modification according to the present invention. In this implementation, dynamic bass equalization circuit 20 includes a second order Sallen-Key high pass filter 50, which is also referred to as a voltage controlled voltage source high pass filter, with a parallel pair of reversed diodes 52 and 54 coupled across a real-time negative feedback path. As is known in the art, a second order filter provides frequency dependent filtering at about -12dB/octave.

Sallen-Key high pass filter 50 is an active filter circuit in which controlling the amplitude voltage across an element will beneficially change the frequency response, Q, or gain of the circuit according to the present invention. Dynamic bass equalization circuit 20 could alternatively include in substitution of Sallen -Key high pass filter 50 any active filter circuit in which controlling the amplitude voltage across an element will change the frequency response, Q, or gain of the circuit, as described below in greater detail.

Sallen-Key high pass filter 50 includes a pair of series connected capacitors 60 and 62 coupled between an equalization circuit input 64 and a positive input 66 of an amplifier 68. A resistor 70 is connected between positive input 66 and ground. A resistor 72 is coupled as a positive feedback path between output 74 and a junction between capacitors 60 and 62. A resistor 76 is connected between ground and a negative input 78 of amplifier 68. A resistor 80 is connected in parallel with reversed diodes 52 and 54 between output 74 and negative input 78 to provide filter 50 with real-time AC negative feedback.

The small signal gain K and quality factor Q characteristics of Sallen-Key high pass filter 50 are represented as:

$$K = 1 + \frac{R_F}{R_1} \quad Q = \frac{1}{3 - K}$$

wherein R1 and RF represent the resistances of resistors 72 and 80, respectively, and Q represents a measure of frequency response modification in terms of the sharpness of the peak gain at a resonant frequency. With increased output voltages, the voltage across resistor 80 will also increase and current will be conducted across diodes 50 and 52, thereby reducing the net impedance between output 74 and negative input 78. A reduction in the gain K will also reduce the quality factor Q of the circuit, thereby providing dynamic frequency response modification.

In one implementation, bass equalization circuit 20 is configured to have a maximum or peak gain at a low frequency value and then to radically roll-off the signal at frequencies below that peak value. This can be achieved with, for example, a quality factor Q greater than 0.707 (i.e., gain K greater than 1.58). With small-signal inputs, bass equalization circuit 20 operates according to the above equations. As the input signal level increases, diodes 50 and 52 begin to reduce the overall gain of equalization circuit 20, as well as reducing its peak gain. The gain becomes asymptotic to unity, and the quality factor Q becomes asymptotic to 0.5. The result is a sub-woofer signal that reduces in level and reduces in peaking as the signal input increases in amplitude.

Fig. 3 is a circuit schematic diagram of a prior art amplitude controlled variable resistance bass equalization circuit 90 having a transistor 92 that is operated in the resistance region of operation. Operation of circuit 90 can be described with reference to its two extremes of operation: transistor 92 being fully ON or fully OFF. With transistor 92 fully ON, circuit 90 operates as a first order high-pass filter with a characteristic frequency  $f_0$  (-3dB) of  $(2\pi RC)^{-1}$ . With transistor 92 fully OFF, there is no high-pass filter operation by circuit 90 and hence no frequency modification. Between the fully ON and OFF states of transistor 92, circuit 90 operates according to the relative proportions of the



ON and OFF states. Circuit 90 suffers from attack and decay time delays due to the filtering and rectification by a diode 96. The attack and decay times can delay bass boost compensation, thereby introducing discernible audio artifacts.

5            Fig. 4 is a circuit schematic diagram of a prior art amplitude compressors/soft clipper circuit 100 having a voltage amplifier circuit 102 with a parallel pair of opposing diodes 104 in the negative feed-back path. At low input signal levels, diodes 104 are off and the circuit amplifies the signal in the usual manner. As the input signal increases, diodes 104 begin to conduct,  
10 logarithmically limiting the voltage across a feedback resistor 106 and reducing the gain of circuit 100 and clamping the maximum voltage to a peak-to-peak value equal to the forward voltage of diodes 104.

Circuit 100 can effectively control the amplitude of the audio output signal and "soft-clip" it before the power-amplifiers "hard-clip". This topology  
15 has advantages over amplitude controlled circuits (e.g., circuit 90) in that it has no attack and decay times associated with its operation. It also can be more linear because it avoids transistor biasing problems. A primary disadvantage of circuit 100 is that there is no control over frequency response. The general frequency response is largely unaffected by the compressing  
20 action. The fundamental frequencies are passed through to the amplifier, and can allow the bass frequencies to cause over-excursion in the driver.

Dynamic bass equalization circuit 20 operates without the attack or decay times that are characteristic of prior amplitude controlled variable resistance bass equalization circuits (e.g., circuit 90). In addition, bass  
25 equalization circuit 20 has amplitude dependent gain and frequency response modification that are not available in prior amplitude compressors/soft clipper bass equalization circuit (e.g., circuit 100). Hence, dynamic bass equalization circuit 20 eliminates distortion and audio artifacts characteristic of prior bass

equalization circuits. Moreover, the distortion properties of bass equalization circuit 20 are symmetrical, and thus harmonic in nature. For sub-woofer circuits, the use of any high order low pass filter after bass equalization circuit 20 can reduce the harmonics to inaudible levels.

5            Fig. 5 is a circuit schematic diagram of another dynamic bass equalization circuit 120 with amplitude dependent gain and frequency response modification according to the present invention. Bass equalization circuits 20 and 120 are substantially the same, with like components identified by like reference numerals, except that circuit 120 includes resistors 122 and  
10    124 in the positive feedback path. Resistors 122 and 124 function as a voltage divider that reduces circuit sensitivity to component values and deviations. As a result of resistors 122 and 124, the remaining circuit components would have values designed for the voltage divided gains.

          In one exemplary implementation, amplifier 68 is of type TL072/301/TI  
15    and diodes 52 and 54 are of type 1N4148. Capacitors 60 and 62 each have a capacitance of 47 nF. Resistors 70, 72, 76, and 80 have resistances of 91 k-ohms, 68 k-ohms, 10 k-ohms, 91 k-ohms, respectively. Voltage divider resistors 122 and 124 have resistances of 62 k-ohms and 16 k-ohms, respectively.

20            Based upon the Sallen-Key key equations, circuit 120 without diodes 52 and 54 has a designed gain of 20dB, a Q-factor of 1.2, and a frequency roll-off at 40Hz. At the Q point of the circuit, or the point of maximum gain, the gain is approximately 22.5dB. The non-peaked pass-band gain is 20dB.

          Equalization circuit 120 will have a theoretical pass-band compression  
25    near 20dB because as the input voltage increases, the forward voltage across diodes 52 and 54 becomes very small and causes the net feedback impedance to be very small, and the gain will become asymptotic to unity. Although this circuit has maximum compression, the THD or Total Harmonic

Distortion is also high. (Total Harmonic Distortion is a measure of the amount a circuit distorts a sinusoidal signal applied to the circuit of the resultant signal.) This high THD arises because diodes 52 and 54 function as voltage clamps. At small signals, diodes 52 and 54 do not conduct, and variations in the waveform are very linear. However, as the peak of the waveform increases, diodes 52 and 54 begin conducting and reducing the gain of the circuit for only part of the waveform, thereby causing non-linear deformation. The distortion is harmonic in nature and can be filtered out with the use of a low-pass filter stage.

Fig. 6 is a circuit schematic diagram of another dynamic bass equalization circuit 130 with amplitude dependent gain and frequency response modification according to the present invention. Bass equalization circuits 120 and 130 are substantially the same, with like components identified by like reference numerals, except that circuit 130 includes a resistor 132 connected in series with diodes 52 and 54 in the negative feedback path.

With high amplitude input signals, the forward voltage across diodes 52 and 54 is small in comparison to the voltage across resistor 80. When diodes 52 and 54 are fully on, series resistor 132 is essentially in parallel with resistor 80. Although it reduces the total compression and frequency modification, series resistor 132 reduces the amount of total harmonic distortion (THD) because the nonlinear diode forward voltage is a small fraction of the overall feedback path voltage drop. In an implementation with resistors each having a resistance of 91 k-ohms, the net gain will be roughly half. In practice, the gain has a factor of 1+ that needs to be accounted for, and the diode forward voltage has the net effect of increasing the impedance in the parallel leg, thus the total amount of compression is closer to about 5.5dB. This implementation has much less distortion than the previous example, at the

expense of less compression. The lower THD is result of the fact that when diodes 52 and 54 are conducting, the linear circuit element (i.e., series resistor 132) is conducting in the normal linear fashion. The diode forward voltage is much smaller than the voltage across series resistor 132 during most of the waveform, thus it's non-linear effects are minimized.

Simulation and testing of circuits 120 and 130 showed that both the gain and quality factor Q of the circuits were being modified by diodes 52 and 54. Simulation and testing was also performed with respect to a circuit 120 with diodes. At 10mv input, circuits 120 and 130 and circuit 120 without diodes performed almost identically. They all had a frequency response plot that coincided with the mathematically predicted results of the standard Sallen-Key filter.

At 100mv input, the circuit 120 with no diodes performed mathematically. Circuit 120 at 100mv input has a measured peak amplitude gain of 12.9dB at 100Hz and 11.7dB at 40Hz for compression values of 7.2dB and 10.8dB respectively. At 1.0 volts input, the gain was 1.8dB at 100Hz and -1.4dB at 40Hz for compression values of 18.3dB and 23.9dB respectively. Circuit 130 had a peak amplitude gain of 17.1dB at 100 Hz and 15.5dB at 40Hz. This is a resultant compression of about 3dB and 7dB at 100Hz and 40 Hz respectively. If the Q were not being modified, the two compression values would be the same. At 1.0 volt input, the gain of circuit 130 is reduced to 14.6dB at 100Hz and 11.2dB at 40Hz for compression values of about 5.5dB and 11.3dB, respectively.

Fig. 7 is a circuit schematic diagram of another dynamic bass equalization circuit 150 with amplitude dependent gain and frequency response modification according to the present invention. Bass equalization circuit 150 includes a fourth order Sallen-Key high pass filter 152, which is also referred to as a voltage controlled voltage source high pass filter, with a

parallel pair of reversed diodes 154 and 156 coupled in series with a resistor 158 across a negative feedback path. As is known in the art, a fourth order filter provides frequency dependent filtering at about  $-24\text{dB/octave}$ .

Sallen-Key high pass filter 152 is an active filter circuit in which  
5 controlling the amplitude voltage across an element will beneficially change the frequency response, Q, or gain of the circuit according to the present invention. Fourth order Sallen-Key high pass filter 152 operates in a manner very similar to that of the 2nd order Sallen-Key circuits described with reference to bass equalization circuits 20, 120, and 130. Dynamic bass  
10 equalization circuit 150 illustrates yet another active filter circuit 152 in which controlling the amplitude voltage across an element will change the frequency response, Q, or gain of the circuit.

Fig. 8 is a circuit schematic diagram of another dynamic bass equalization circuit 170 with amplitude dependent gain and frequency  
15 response modification according to the present invention. Bass equalization circuit 170 includes a multiple feedback band-pass filter 172 that includes a parallel pair of reversed diodes 174 and 176 coupled in series with a resistor 178 across a negative feedback path. Multiple feedback band-pass filter 172 is an active filter circuit in which controlling the amplitude voltage across an  
20 element will beneficially change the frequency response, Q, or gain of the circuit according to the present invention.

In the implementation illustrated in Fig. 8, bass equalization circuit 170 has a gain of 6db and a Q of 1.0 at a center frequency of 50Hz. At low amplitude levels, diodes 174 and 176 are off and multiple feedback band-pass  
25 filter 172 operates in its conventional manner. At large amplitude levels, diodes 174 and 176 conduct placing the resistor in parallel 178 with a feedback resistor 180. The Q and gain reduce asymptotically to 0.3 and  $-14\text{dB}$ , respectively, and the center frequency increases to 150 Hz.

In addition to multimedia computer systems with sub-woofer drivers as shown in Fig. 1, it will be appreciated that dynamic bass equalization circuits according to the present invention may be also be applied to with full range drivers. Fig. 9 is a block diagram of one stereo channel of such an  
5 implementation.

It will also be appreciated that more powerful amplitude and Q compression can be attained by using Unequal-Value Sallen-Key circuits. Because all of the Sallen-Key forms are of the non-inverting KRC class, the gain K will always be asymptotic to unity. Thus an Unequal-Value Sallen-Key  
10 circuit designed for a gain of 20dB will have compression that, in theory, will be 20dB. For Unequal-Value circuits, the Q compression is asymptotic, but the absolute value is dependent on the other circuit values. This type of dynamic compression can be applied to other active filter topologies as well.

In view of the many possible embodiments to which the principles of  
15 our invention may be applied, it should be recognized that the detailed embodiments are illustrative only and should not be taken as limiting the scope of our invention. Rather, the invention includes all such embodiments as may come within the scope and spirit of the following claims and equivalents thereto.

Claims

1. In a multimedia computer amplified speaker system having a speaker driver for transducing into sound an audio electrical signal that has an amplitude, the improvement comprising:

a dynamic bass equalization circuit with a second or higher order active filter having a dynamically adjusted gain and frequency response that vary with the amplitude of the audio electrical signal.

2. The speaker system of claim 1 in which the active filter includes a Sallen-Key high pass filter.

3. The speaker system of claim 1 in which the dynamically adjusted gain and frequency response are provided by a parallel pair of reversed diodes.

4. The speaker system of claim 1 in which the active filter includes an amplifier with a negative feedback path that includes a parallel pair of opposed diodes.

5. The speaker system of claim 4 in which the amplifier includes an output and the negative feedback path includes a resistor connected in series with the parallel pair of opposed diodes and the amplifier output.

6. The speaker system of claim 4 in which the amplifier includes a positive feedback path having a voltage divider that voltage divides a feedback voltage.

7. The speaker system of claim 6 in which the amplifier includes an output and the negative feedback path includes a resistor connected in series with the parallel pair of opposed diodes and the amplifier output.

8. The speaker system of claim 1 in which the active filter includes an amplifier with a positive feedback path having a voltage divider that voltage divides a feedback voltage.

9. The speaker system of claim 1 in which the dynamic bass

equalization circuit provides a bass equalized audio signal that is delivered to a sub-woofer speaker driver.

10. The speaker system of claim 1 in which the dynamic bass equalization circuit provides a bass equalized audio signal that is delivered to a full-range speaker driver.

11. In a multimedia computer amplified speaker system having a speaker driver for transducing into sound an audio electrical signal that has an amplitude, the improvement comprising:

- a dynamic bass equalization circuit with a second or higher order Sallen-Key high pass filter having a dynamically adjusted gain and frequency response that vary with the amplitude of the audio electrical signal.

12. The speaker system of claim 11 in which the dynamically adjusted gain and frequency response are provided by a parallel pair of reversed diodes.

13. The speaker system of claim 11 in which the active filter includes an amplifier with a negative feedback path that includes a parallel pair of opposed diodes.

14. The speaker system of claim 13 in which the amplifier includes an output and the negative feedback path includes a resistor connected in series with the parallel pair of opposed diodes and the amplifier output.

15. The speaker system of claim 13 in which the amplifier includes a positive feedback path having a voltage divider that voltage divides a feedback voltage.

16. The speaker system of claim 15 in which the amplifier includes an output and the negative feedback path includes a resistor connected in series with the parallel pair of opposed diodes and the amplifier output.

17. The speaker system of claim 11 in which the active filter includes an amplifier with a positive feedback path having a voltage divider that voltage



divides a feedback voltage.

18. The speaker system of claim 11 in which the dynamic bass equalization circuit provides a bass equalized audio signal that is delivered to a sub-woofer speaker driver.

5        19. The speaker system of claim 11 in which the dynamic bass equalization circuit provides a bass equalized audio signal that is delivered to a full-range speaker driver.

10        20. In a multimedia computer amplified speaker system having a speaker driver for transducing into sound an audio electrical signal that has an amplitude, the improvement comprising:

a dynamic bass equalization circuit with an active filter having a dynamically adjusted gain and frequency response that vary with the amplitude of the audio electrical signal.

15        21. The speaker system of claim 20 in which the dynamically adjusted gain and frequency response are provided by a parallel pair of reversed diodes.

22. The speaker system of claim 20 in which the active filter includes an amplifier with a negative feedback path that includes a parallel pair of opposed diodes.

20        23. The speaker system of claim 22 in which the amplifier includes an output and the negative feedback path includes a resistor connected in series with the parallel pair of opposed diodes and the amplifier output.

25        24. The speaker system of claim 22 in which the amplifier includes a positive feedback path having a voltage divider that voltage divides a feedback voltage.

25. The speaker system of claim 24 in which the amplifier includes an output and the negative feedback path includes a resistor connected in series with the parallel pair of opposed diodes and the amplifier output.

26. The speaker system of claim 20 in which the active filter includes an amplifier with a positive feedback path having a voltage divider that voltage divides a feedback voltage.

5 27. The speaker system of claim 20 in which the dynamic bass equalization circuit provides a bass equalized audio signal that is delivered to a sub-woofer speaker driver.

28. The speaker system of claim 20 in which the dynamic bass equalization circuit provides a bass equalized audio signal that is delivered to a full-range speaker driver.

10 29. In a multimedia computer amplified speaker system having a speaker driver for transducing into sound an audio electrical signal that has an amplitude, a method of operating a bass equalization circuit having a gain and a frequency response, comprising:

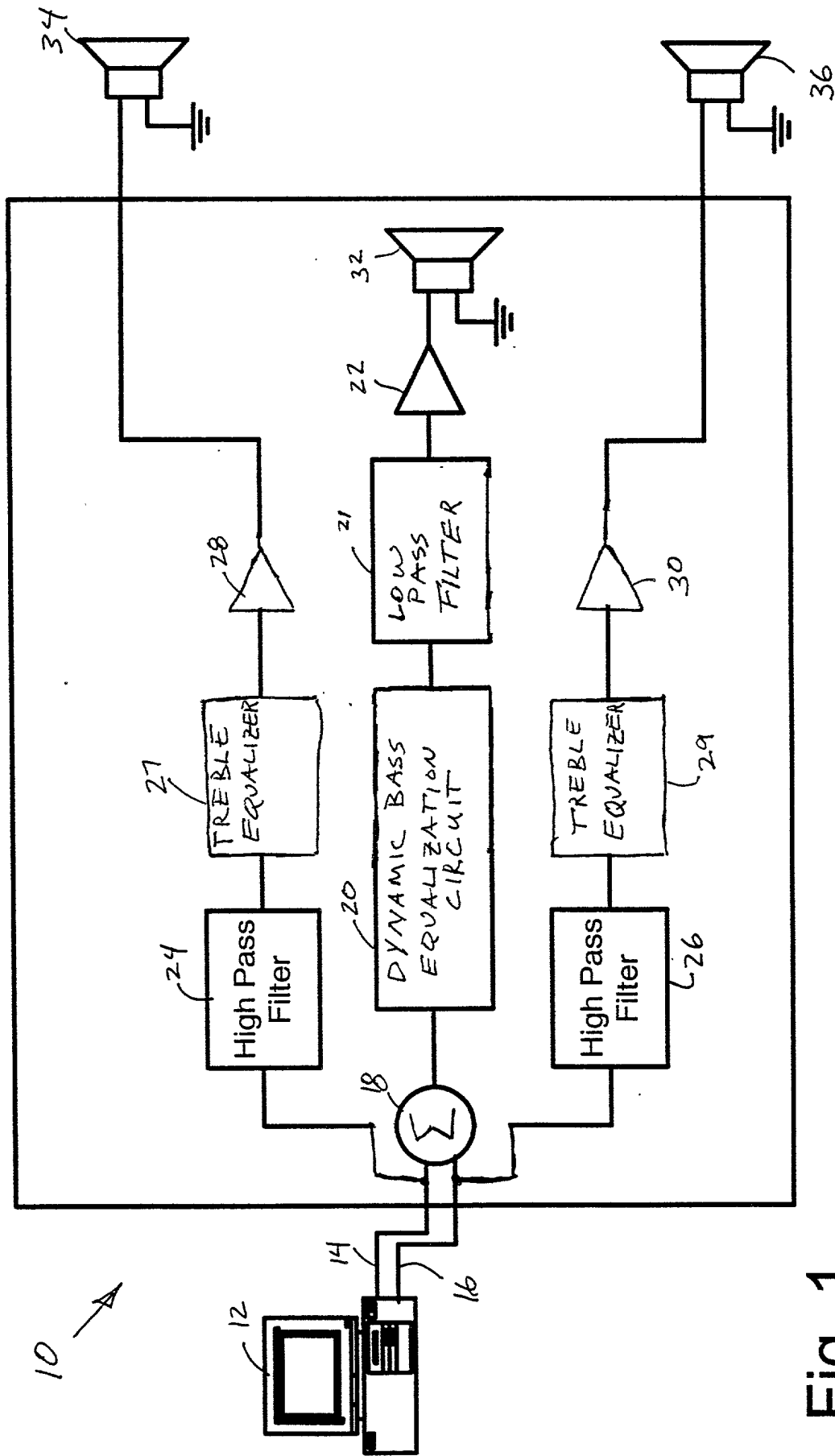
15 dynamically adjusting the gain of the bass equalization circuit according to the amplitude of the audio electrical signal to provide an amplitude dependent gain; and

dynamically adjusting the frequency response of the bass equalization circuit according to the amplitude dependent gain.

20 30. The method of claim 31 in which dynamically adjusting the frequency response of the bass equalization circuit includes passing a negative feedback signal through a parallel pair of opposed diodes.

Abstract of the Invention

A dynamic bass equalization circuit has an amplitude dependent gain  
5 that is dependent upon the audio electrical signal amplitude and a dynamically  
adjusted frequency response that varies with the amplitude dependent gain.  
In one implementation, the dynamic bass equalization circuit includes a  
Sallen-Key high pass filter that includes an amplifier with a negative feedback  
path. The dynamically adjusted frequency response is provided by a parallel  
10 pair of reversed diodes connected in the negative feedback path.



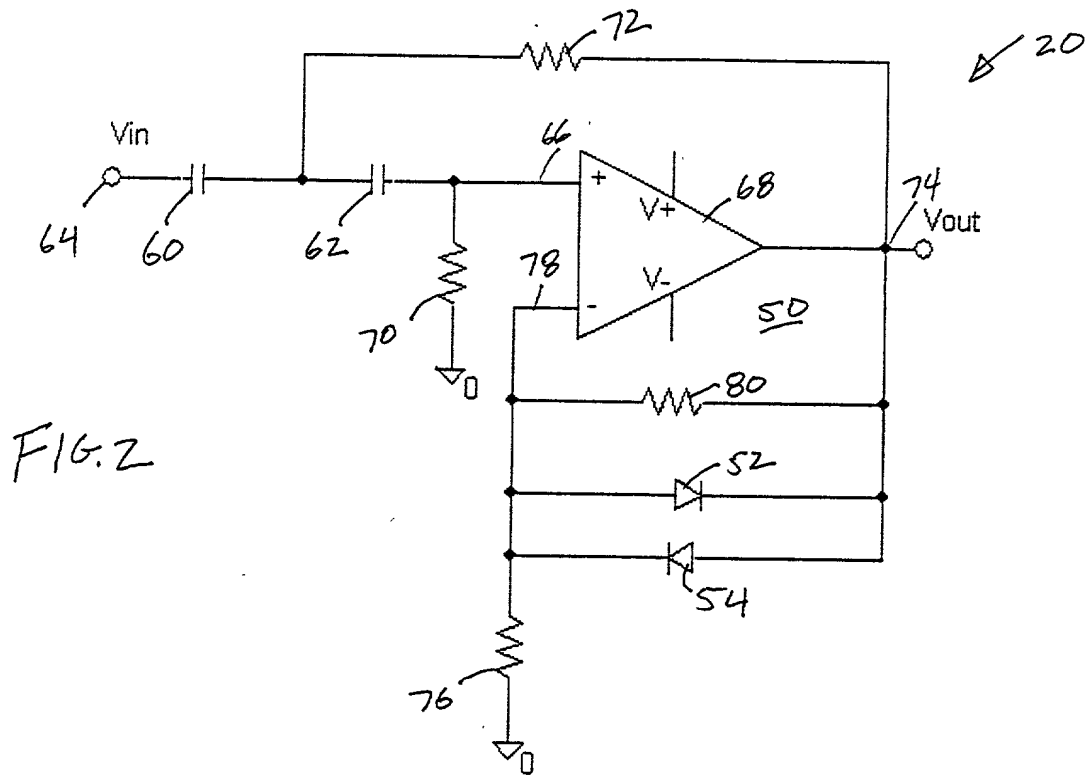


FIG. 3 - PRIOR ART

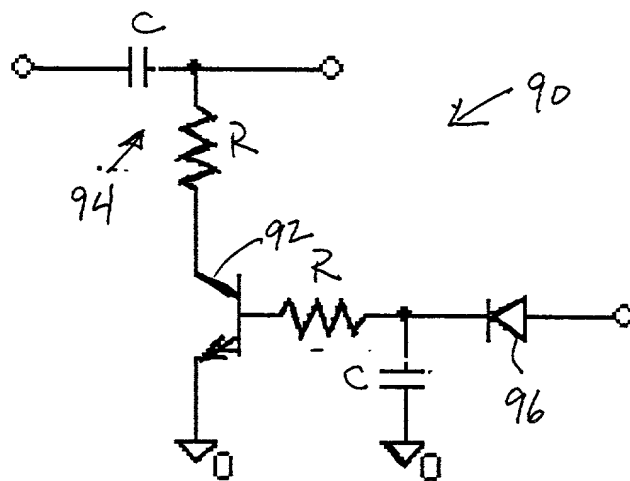


FIG. 4- PRIOR ART

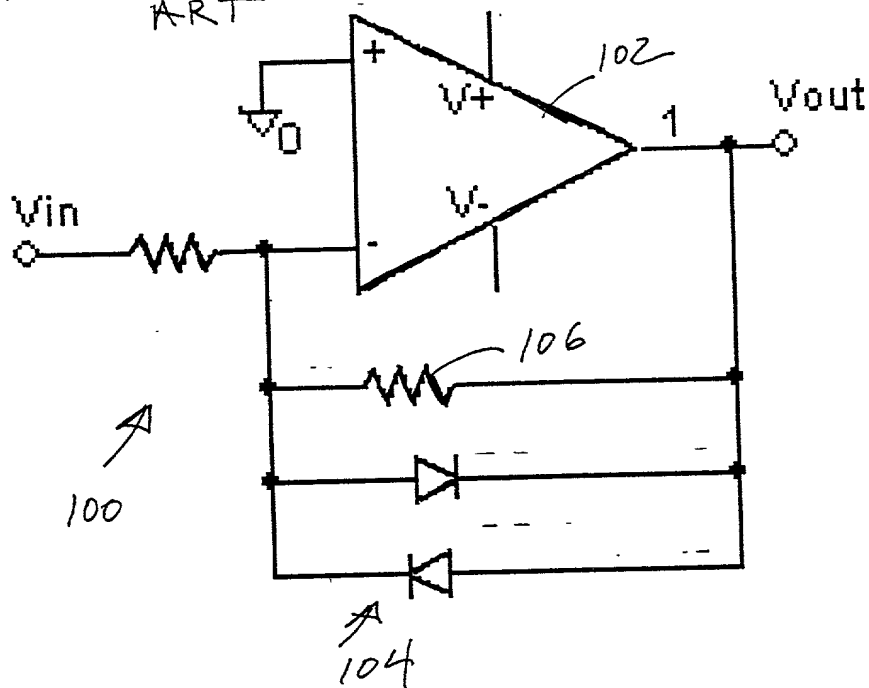


FIG. 5

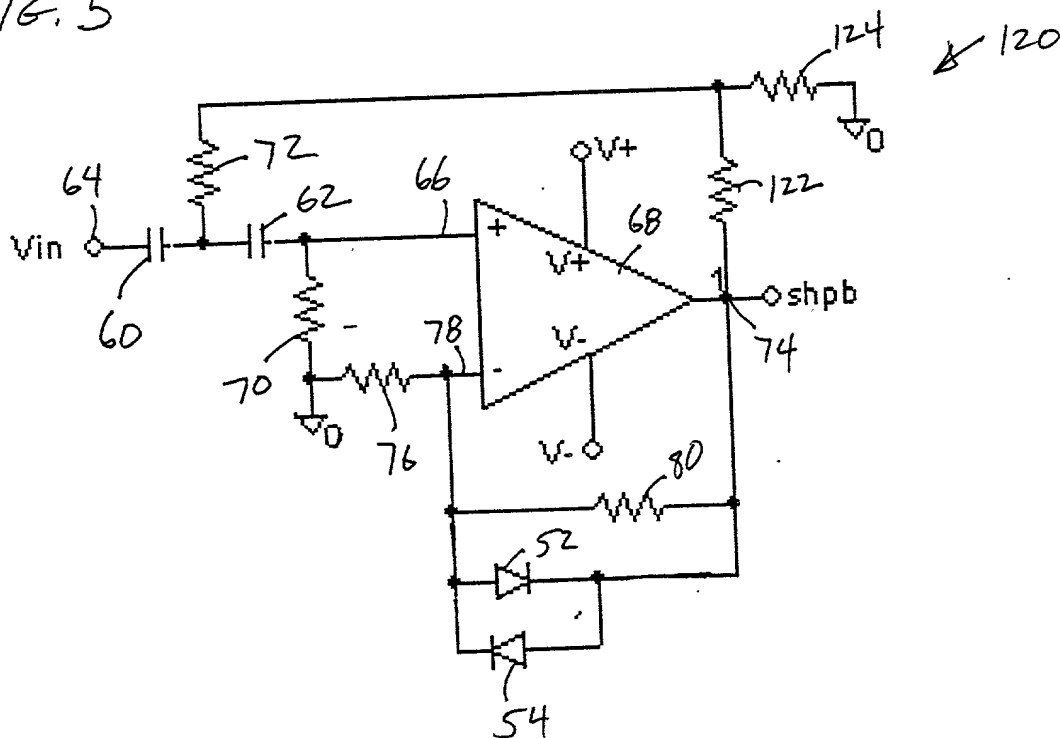


FIG. 6

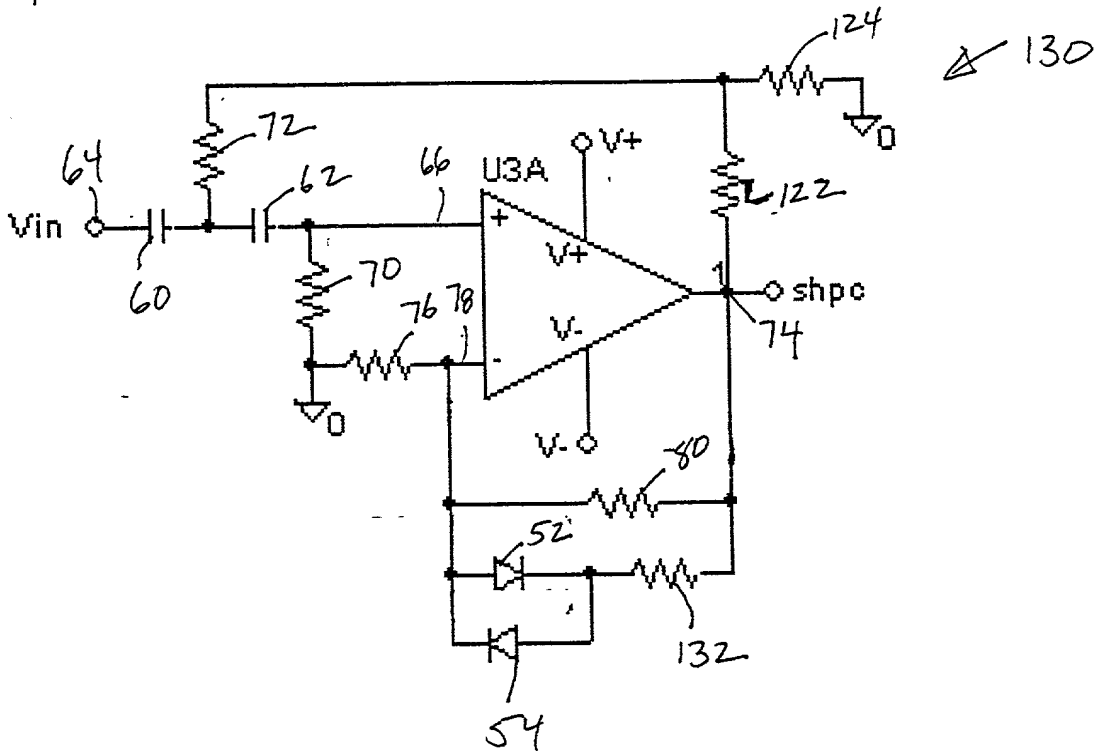
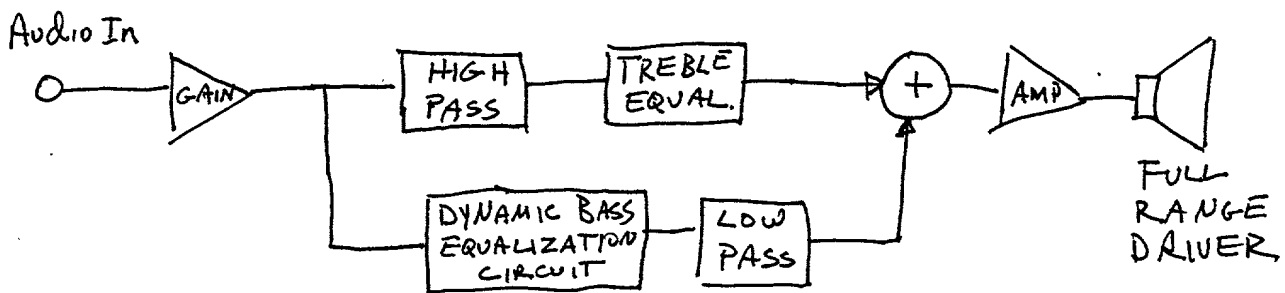


FIG. 7



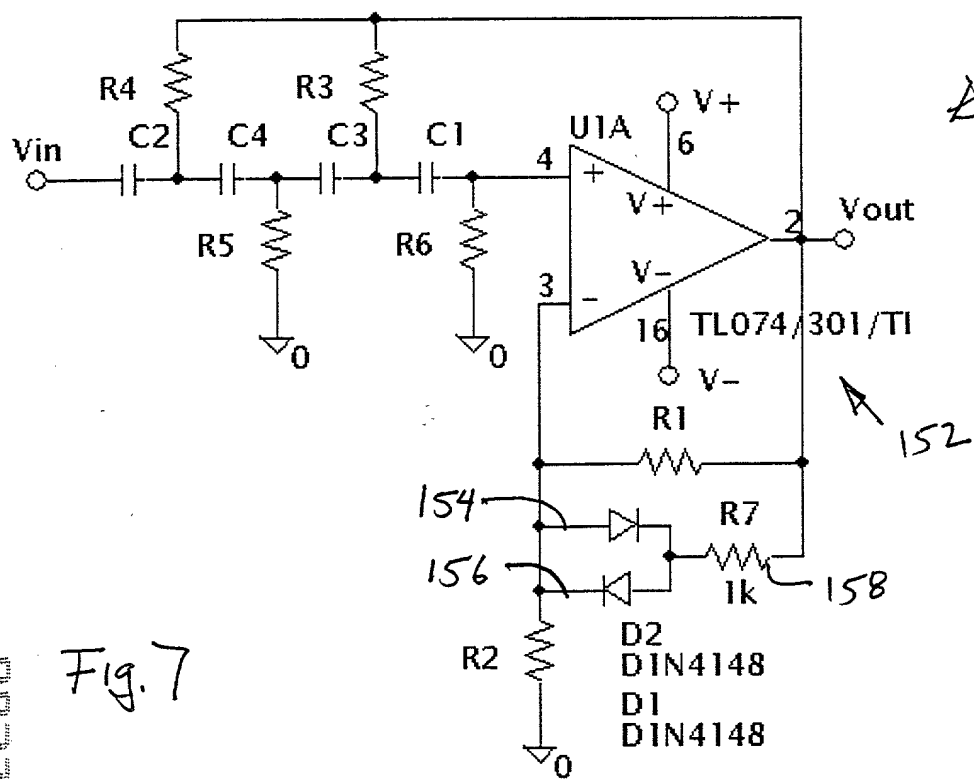


Fig. 7

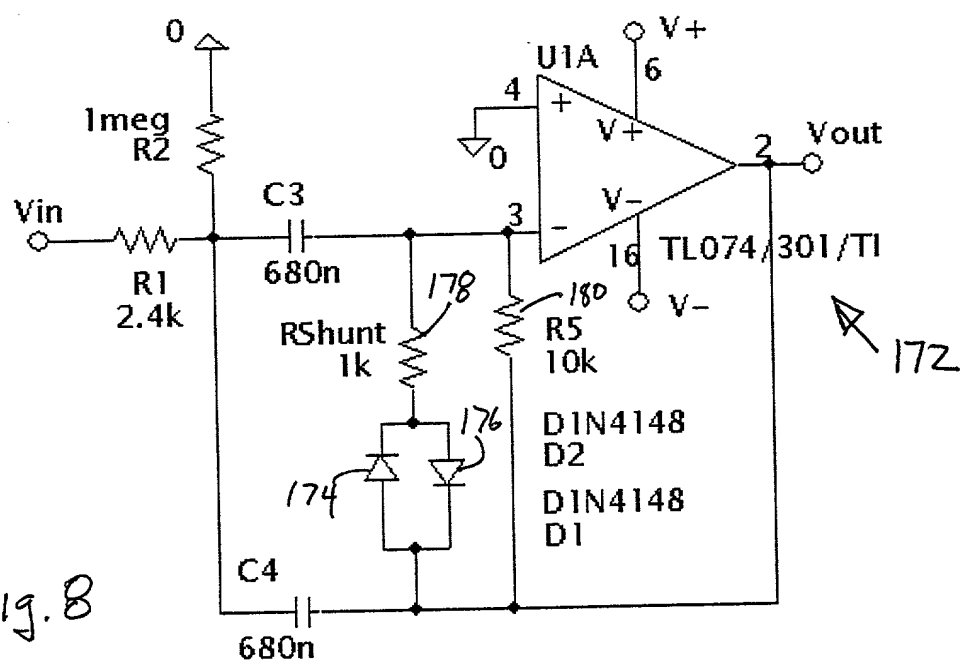


Fig. 8



# COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled Dynamic Bass Equalization with Modified Sallen-Key High Pass Filter, the specification of which

- ☒ is attached hereto.  
☐ was filed on \_\_\_\_\_ as Application No. \_\_\_\_\_.  
☐ was described and claimed in PCT International Application No. \_\_\_\_\_, filed on \_\_\_\_\_, and as amended under PCT Article 19 on \_\_\_\_\_ (if applicable).  
☐ and was amended on \_\_\_\_\_ (if applicable).  
☐ with amendments through \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56. If this is a continuation-in-part application filed under the conditions specified in 35 U.S.C. § 120 which discloses and claims subject matter in addition to that disclosed in the prior copending application, I further acknowledge the duty to disclose material information as defined in 37 CFR § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or of any PCT International application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT International application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
_____	_____	_____	<input type="checkbox"/> <input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	Yes No

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

_____	_____
(Application No.)	(Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or § 365(c) of any PCT International application(s) designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT International filing date of this application:

_____	_____	_____
(Application No.)	(Filing Date)	(Status: patented, pending, abandoned)

The undersigned hereby authorizes the U.S. attorney or agent named herein to accept and follow instructions from \_\_\_\_\_ as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between the U.S. attorney or agent and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorney or agent named herein will be so notified by the undersigned.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application, to file a corresponding international application, and to transact all business in the Patent and Trademark Office connected therewith:

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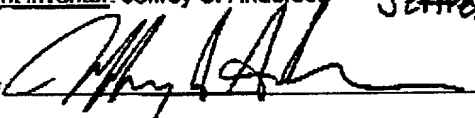
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Jeffrey S. Anderson

JEFFREY S. ANDERSON

Inventor's signature



Date

8/24/1999

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